Galileo I/O Mappings

| Arduino IDE ID | GPIO | | | PWM | Int | Dir | Muxed with | Initial Setup |
	Source	Pin	Linux	Linux				
IO0	Cypr	GPORT4_BIT6_PWM2	50	N/A	-	BI	UART0_RXD	I w/ pullup off
IO1	Cypr	GPORT4_BIT7_PWM0	51	N/A	-	BI	UART0_TXD	I w/ pullup off
IO2	SoC (Cypr)	GPIO<6> (GPORT0_BIT4_PWM7)	14 (32*)	-	0	BI	-	I w/ pullup off
IO3	SoC (Cypr)	GPIO<7> (GPORT0_BIT2_PWM3)	15 (18*)	3	1	BI	(PWM)	I w/ pullup off
IO4	Cypr	GPORT1_BIT4_PWM6	28		-	BI	-	I w/ pullup off
IO5	Cypr	GPORT0_BIT1_PWM5	17	5	-	BI	(PWM)	I w/ pullup off
IO6	Cypr	GPORT1_BIT0_PWM6	24	6	-	BI	(PWM)	I w/ pullup off
IO7	Cypr	GPORT1_BIT3_PWM0	27		-	BI	-	I w/ pullup off
IO8	Cypr	GPORT1_BIT2_PWM2	26		-	BI	-	I w/ pullup off
IO9	Cypr	GPORT0_BIT3_PWM1	19	1	-	BI	(PWM)	I w/ pullup off
IO10	Cypr	GPORT0_BIT0_PWM7	16	7	-	BI	(PWM) SPI1_SS_B	I w/ pullup off
IO11	Cypr	GPORT1_BIT1_PWM4	25	4	-	BI	(PWM) SPI1_MOSI	I w/ pullup off
IO12	Cypr	GPORT3_BIT2_PWM3	38		-	BI	SPI1_MISO	I w/ pullup off
IO13	Cypr	GPORT3_BIT3_PWM1	39		-	BI	SPI1_SCK	I w/ pullup off
IO14	Cypr	GPORT4_BIT0_PWM6	44		-	BI	AD7298:VIN0	I w/ pullup off
IO15	Cypr	GPORT4_BIT1_PWM4	45		-	BI	AD7298:VIN1	I w/ pullup off
IO16	Cypr	GPORT4_BIT2_PWM2	46		-	BI	AD7298:VIN2	I w/ pullup off
IO17	Cypr	GPORT4_BIT3_PWM0	47		-	BI	AD7298:VIN3	I w/ pullup off
IO18	Cypr	GPORT4_BIT4_PWM6	48		-	BI	AD7298:VIN4	I w/ pullup off
IO19	Cypr	GPORT4_BIT5_PWM4	49		-	BI	AD7298:VIN5	I w/ pullup off

* See Galileo I/O Function Muxing table below.

Galileo I/O Function Muxing

| Mux Selector | | Cypress GPIO pin | Linux GPIO ID | Dir | Initial Setup |
0	1				
UART0_RXD	IO0	GPORT3_BIT4_PWM7	40	O	unknown
UART0_TXD	IO1	GPORT3_BIT5_PWM5	41	O	unknown
SPI1_SS_B	IO10	GPORT3_BIT6_PWM3	42	O	unknown
SPI1_MOSI	IO11	GPORT3_BIT7_PWM1	43	O	unknown
SPI1_MISO	IO12	GPORT5_BIT2_PWM3	54	O	unknown
SPI1_SCK	IO13	GPORT5_BIT3_PWM1	55	O	unknown
AD7298:VIN0	IO14	GPORT3_BIT1_PWM5	37	O	0
AD7298:VIN1	IO15	GPORT3_BIT2_PWM3	36	O	0
AD7298:VIN2	IO16	GPORT0_BIT7_PWM1	23	O	0
AD7298:VIN3	IO17	GPORT0_BIT6_PWM3	22	O	0
AD7298:VIN4	IO18	GPORT0_BIT5_PWM5	21	O	0
AD7298:VIN5	IO19	GPORT0_BIT4_PWM7	20	O	0
IO2 via SoC GPIO<6>	IO2 via Cypress GPORT0_BIT4_PWM7	GPORT1_BIT7_PWM0	31	O	unknown
IO3 via SoC GPIO<7>	IO3 via Cypress GPORT0_BIT2_PWM3	GPORT1_BIT6_PWM2	30	O	unknown
I2C	(AD7298:VIN4 or IO18) and (AD7298:VIN5 or IO19)	GPORT1_BIT5_PWM4	29	O	1